



PATENT

In the United States Patent and Trademark Office

Applicant: C. Yeh

Applicant's Ref: JLINP161

Application No: 10/617,481

Filed: July 10, 2003

Title: METHOD AND CIRCUIT FOR
SYNCHRONIZING SIGNALS

Examiner: Cox, C.

Group Art Unit: 2816

Date: July 20, 2004

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on July 20, 2004.

Signed: _____

Kay Harlow

Separate Letter to the Official Draftsperson

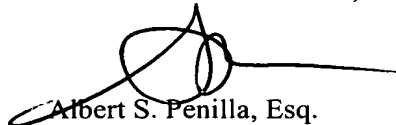
Commissioner for Patents
Mail Stop Issue Fee
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Notice of Allowability dated June 8, 2004, indicating that one corrected formal drawing is required, enclosed herewith is the corrected formal drawing (Figures 1A and 1B), for the above-referenced case. Formal Figures 2, 3, 4A and 4B, submitted on July 10, 2003 remain in this case. Thus, the Office should issue the case with Figures 1A, 1B, 2, 3, 4A and 4B. If the draftsperson has any questions concerning these drawings, please contact the undersigned at the number set forth below. If any fees are due in connection with the filing of these drawings, please charge such fees to deposit account 50-0805 (Order No. JLINP161).

Respectfully submitted,

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